

CYPR-CD01208M



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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF:

Craig NEMECEK

SERIAL NO: 09/989,777

FILED: NOVEMBER 19, 2001

FOR: SLEEP AND STALL IN AN IN-
CIRCUIT EMULATION SYSTEM

:

: GROUP ART UNIT:

:

: EXAMINER:

Handwritten: #4/Pro Aneal4A
9/2/03
3/03

RECEIVED

MAR 07 2003

Technology Center 2100

PRELIMINARY AMENDMENT

COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

Prior to examination of the above-identified application, consideration of the following Amendments and Remarks is respectfully requested.

IN THE SPECIFICATION

Please replace the paragraph beginning at page 12, line 2 with the following:

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Referring now to FIGURE 1, a system 200 is shown that includes a host computer 210 (e.g., a personal computer based on a PENTIUM® class microprocessor) that is interconnected (e.g., using a standard PC interface 214 such as a parallel printer port connection, a universal serial port (USB) connection, etc.) with a base station 218. The host computer 210 generally operates to run an ICE computer program to control the emulation process and further operates in the capacity of a logic analyzer to permit a user to view information provided from the base station 218 for use in analyzing and debugging a system under test or development.